

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Terletzki et al.

Serial No.:

09/659,872

Filed:

Entitled:

9/13/2000

Group Art Unit: 2816

Examiner: M. NGUYEN

Docket No.: 00 P 7882 US

Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

## **Associate Power of Attorney**

Commissioner for Patents Washington, D.C. 20231

Sir:

I hereby appoint as associate attorneys of record the following attorneys:

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Respectfully submitted,

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